

FIG. 1A

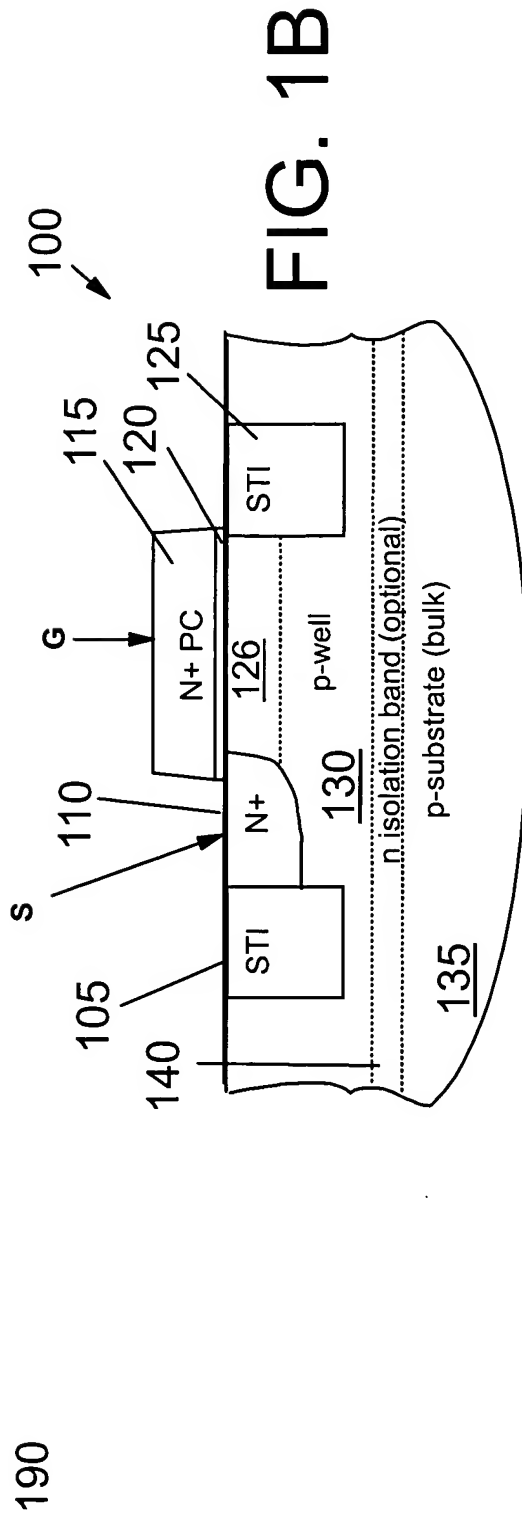


FIG. 1B

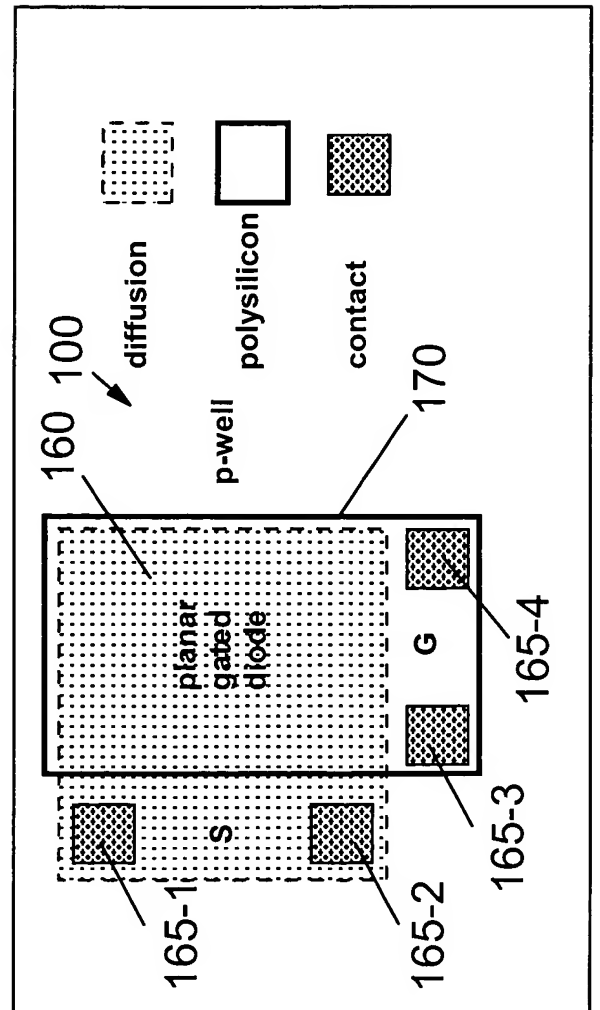


FIG. 1C



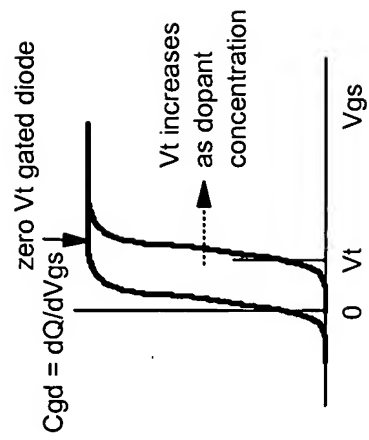


FIG. 3A

Gated Diode Capacitance vs Gate-to-Source Voltage ( $V_{gs}$ )  
 Each curve represents a different gated diode gate size.  
 threshold voltage = 0.2 V

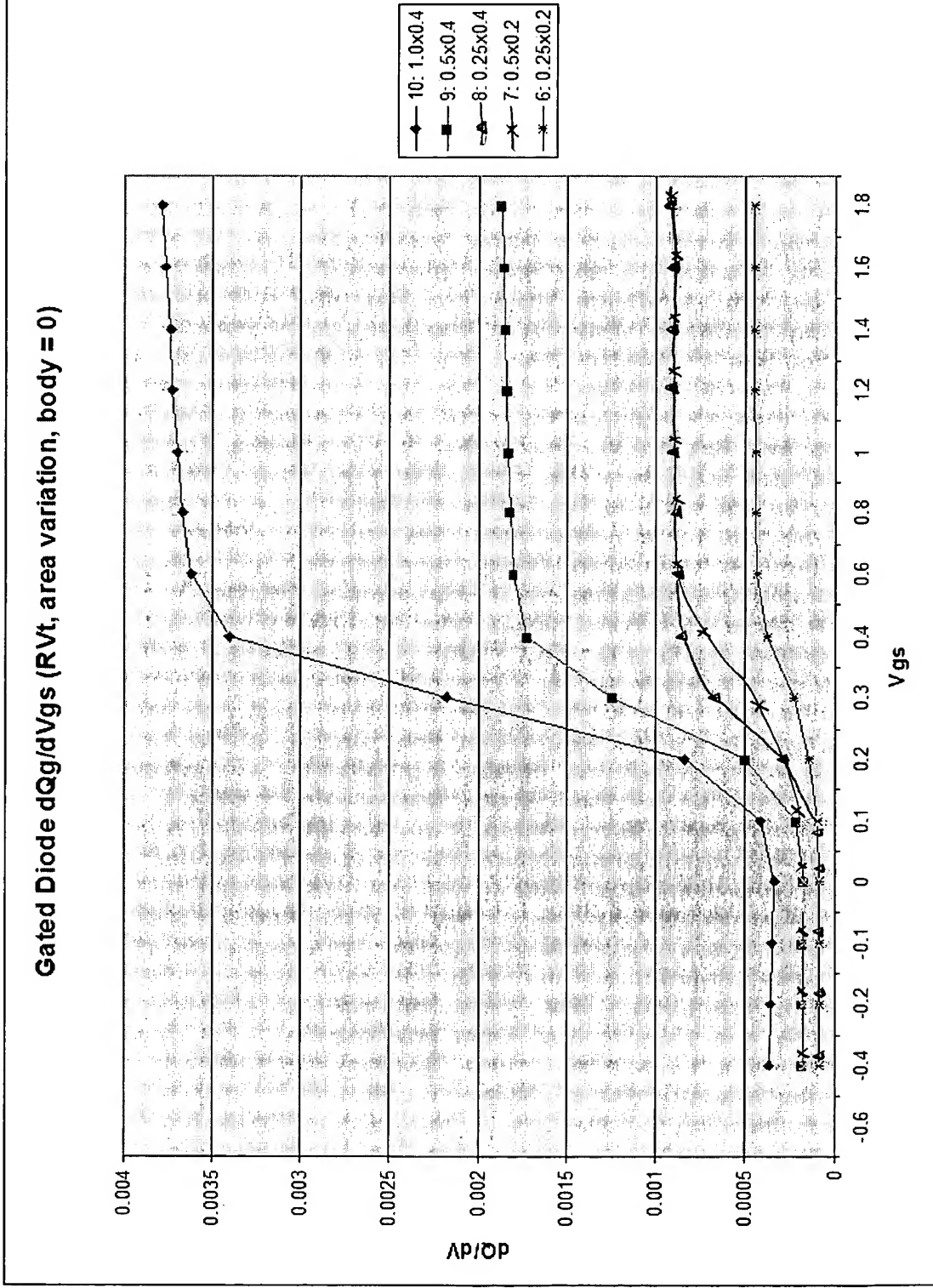
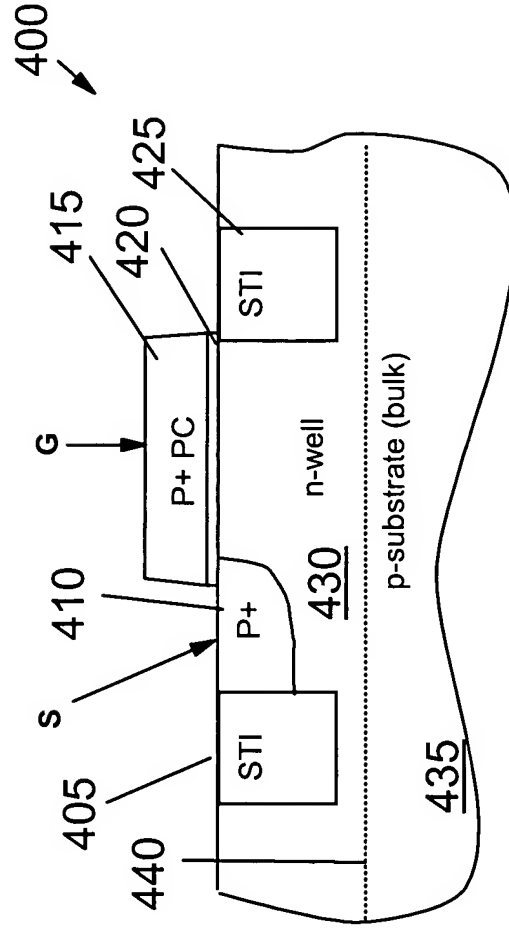
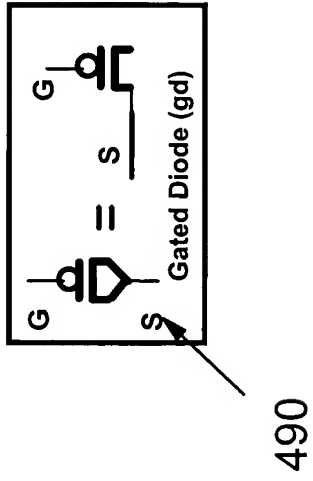


FIG. 3B

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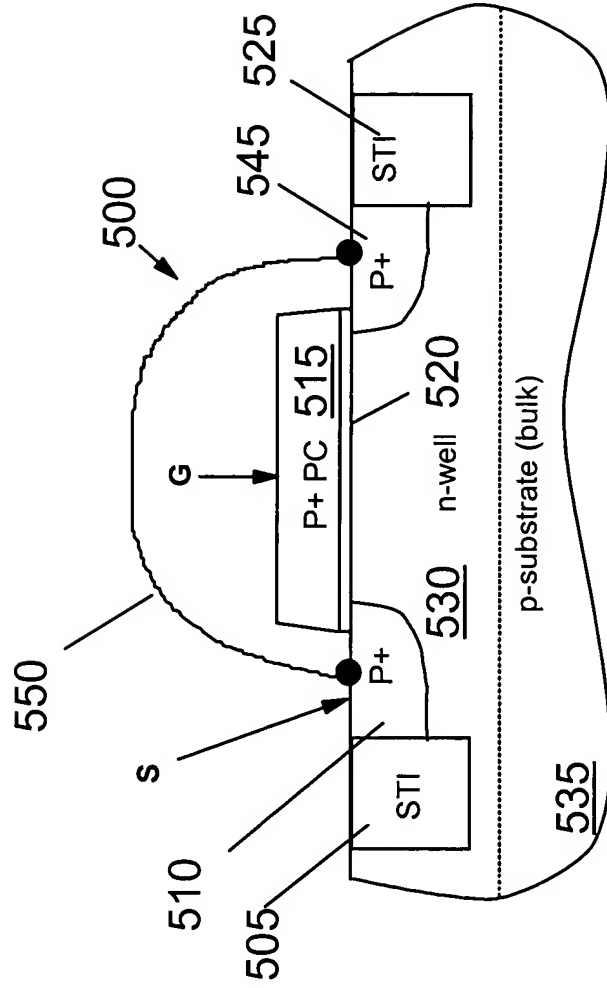
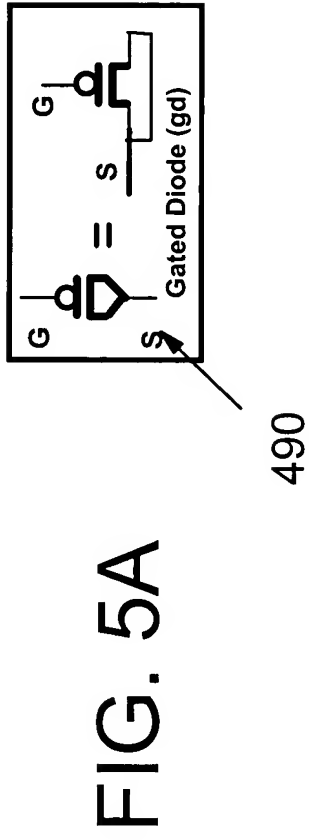


FIG. 5B

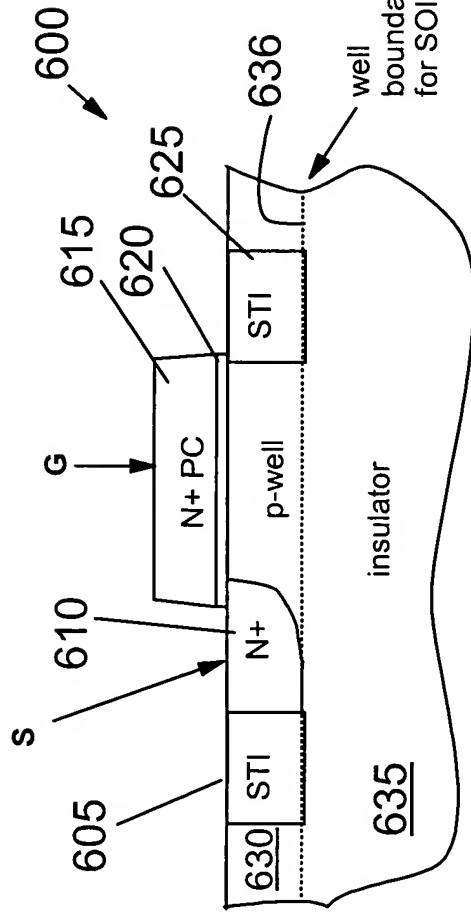


FIG. 6

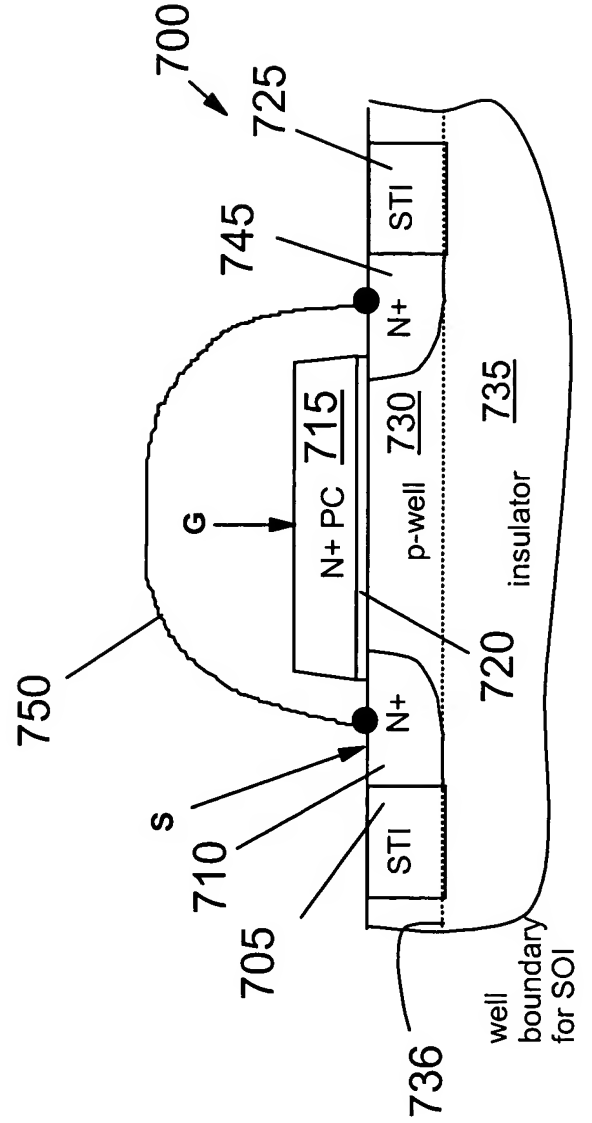


FIG. 7

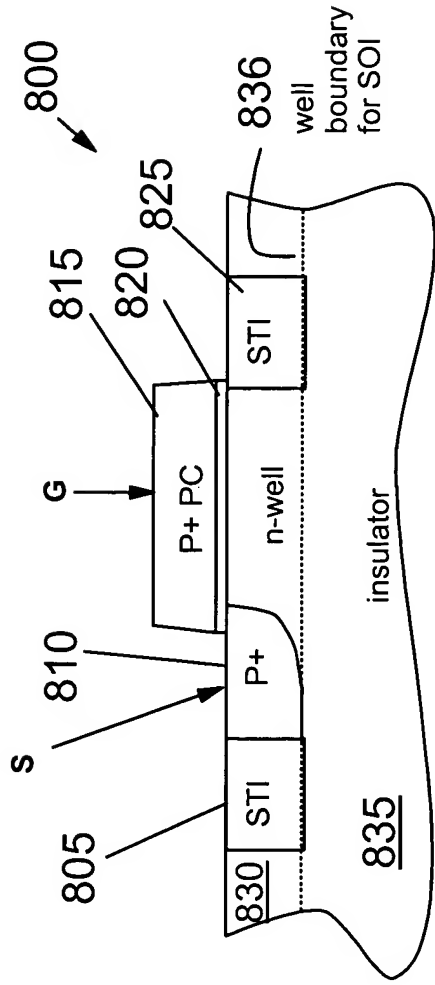


FIG. 8

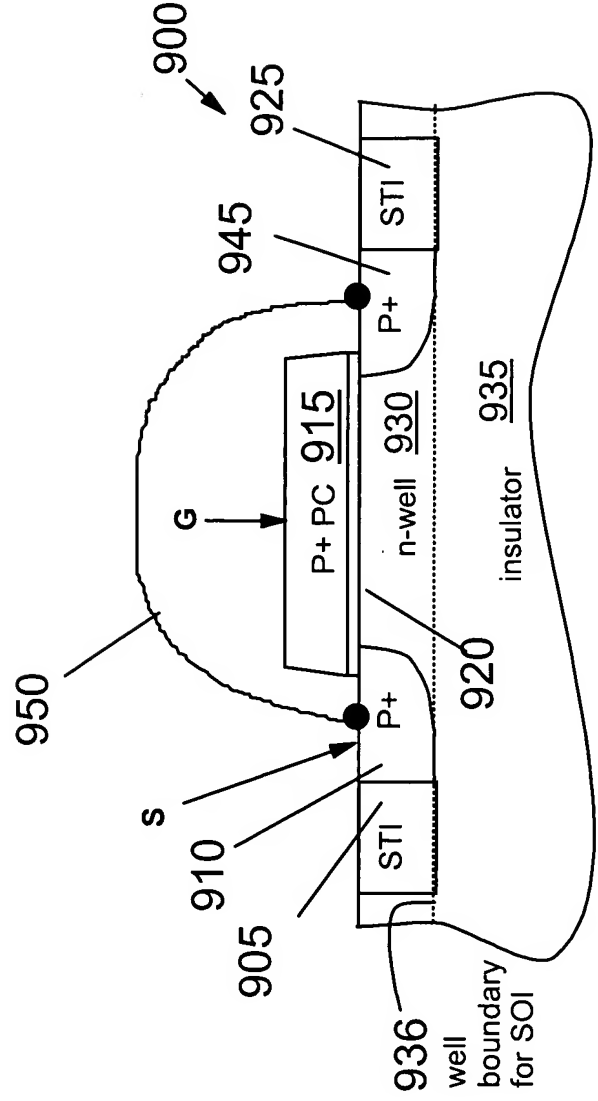
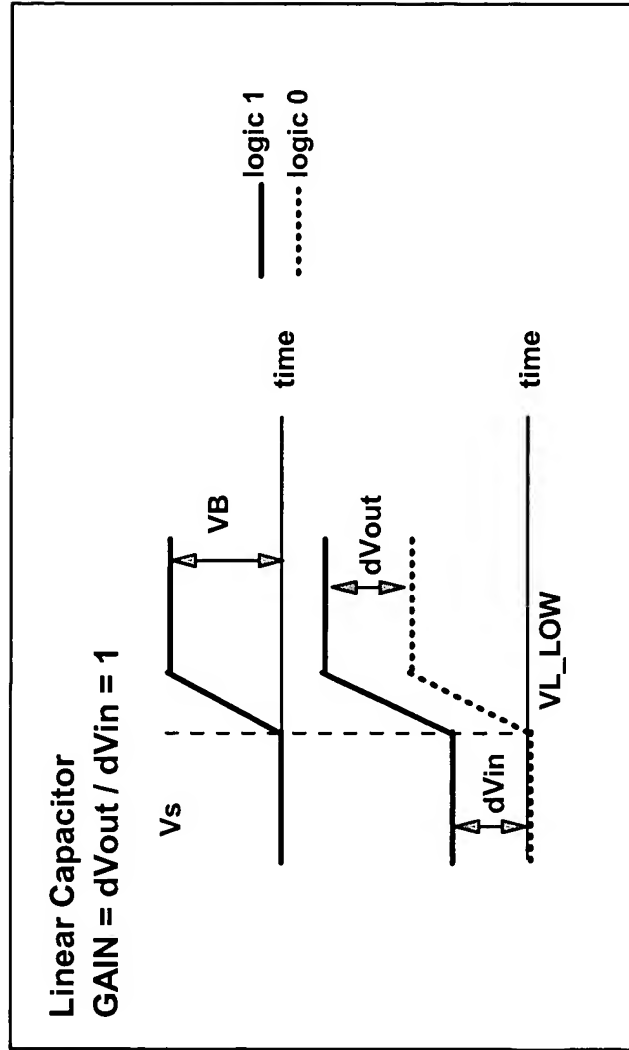


FIG. 9



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FIG. 10



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FIG. 11A

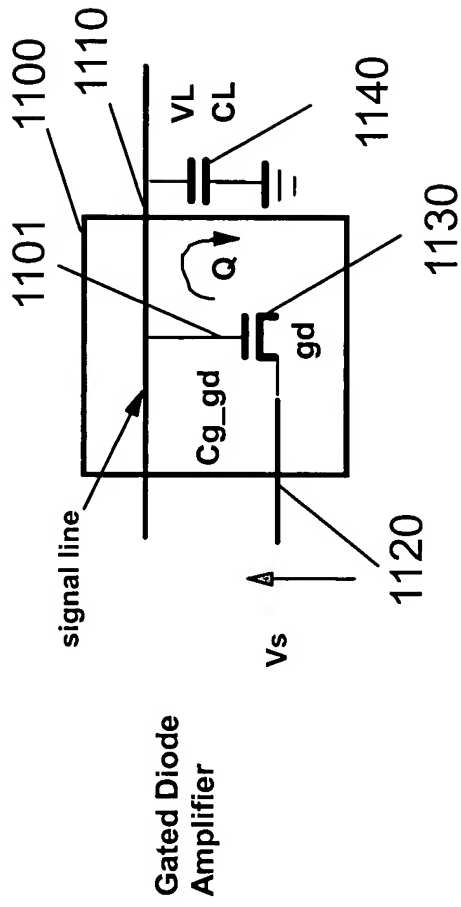


FIG. 11B

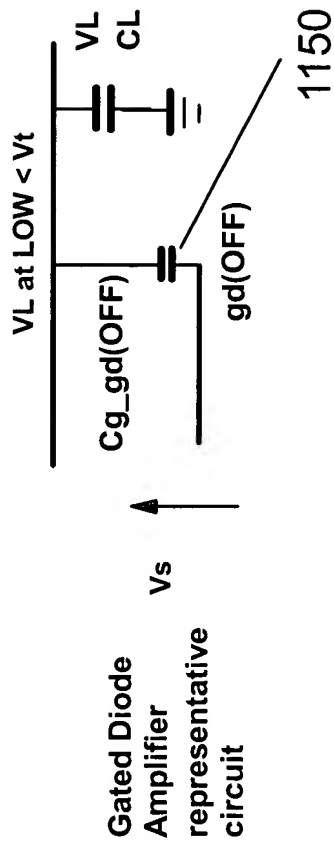
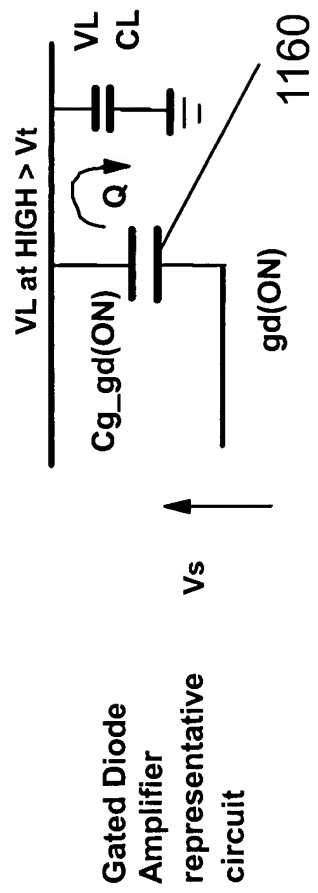


FIG. 11C



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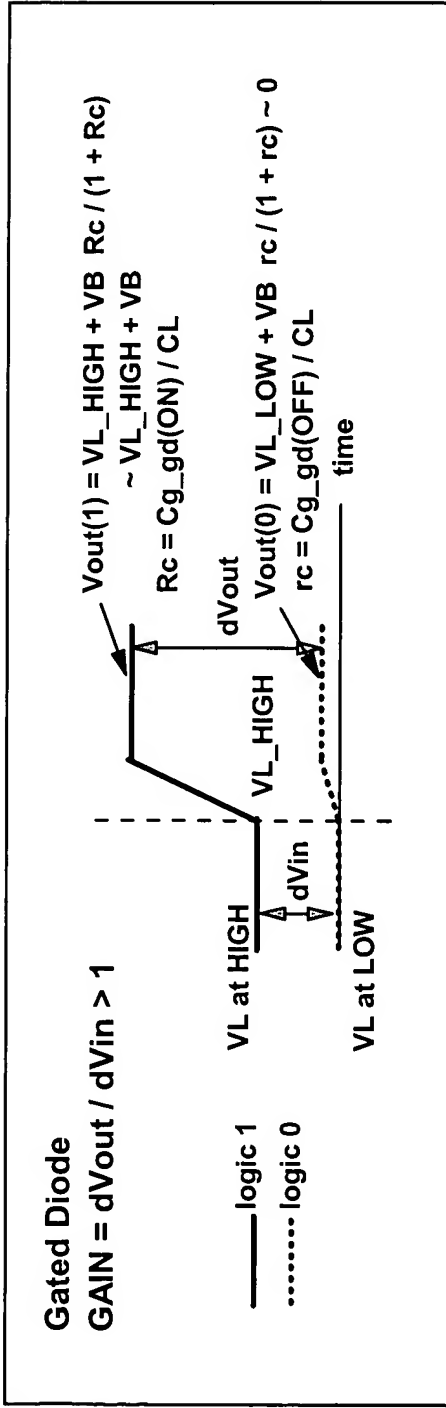


FIG. 12A

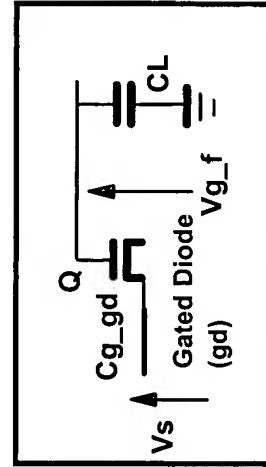


FIG. 12B

FIG. 12C

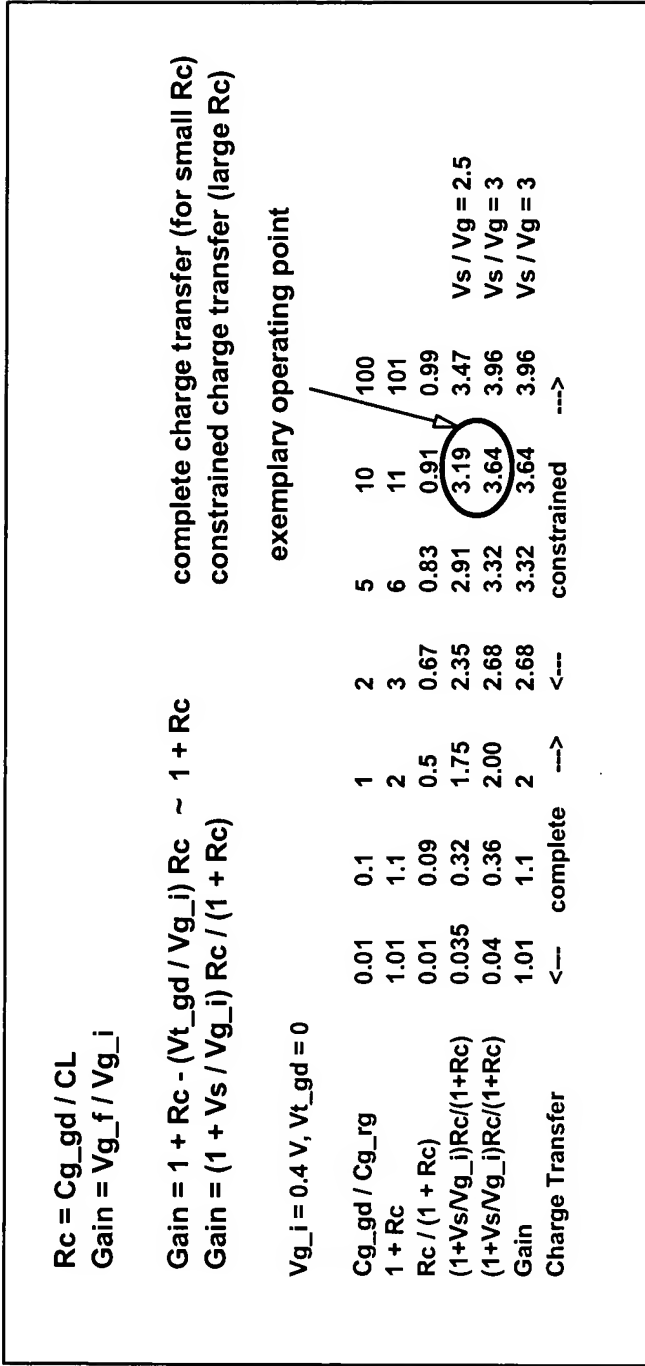
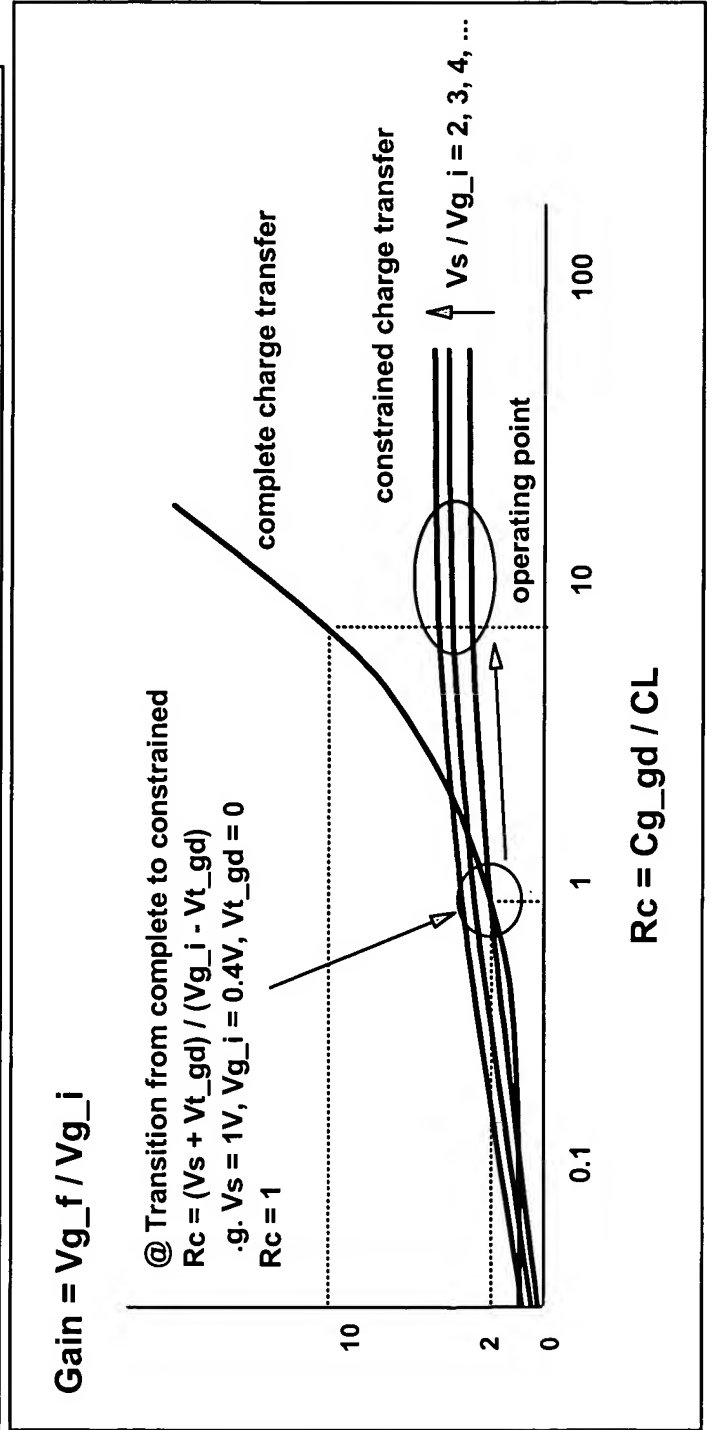


FIG. 12D



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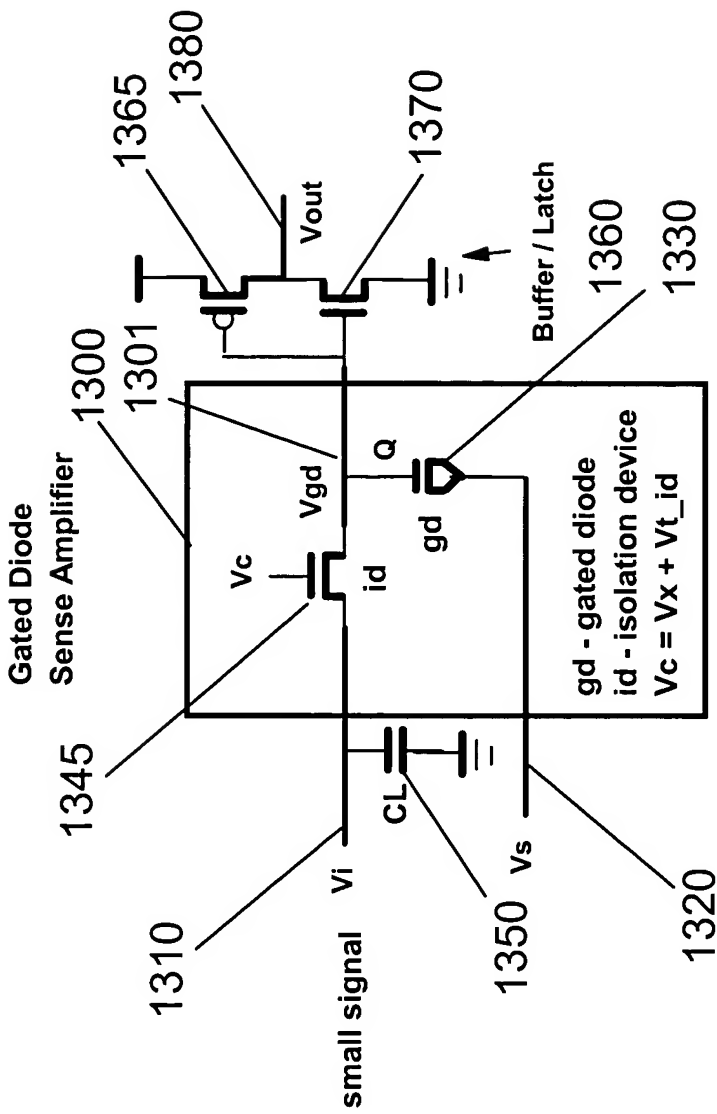


FIG. 13

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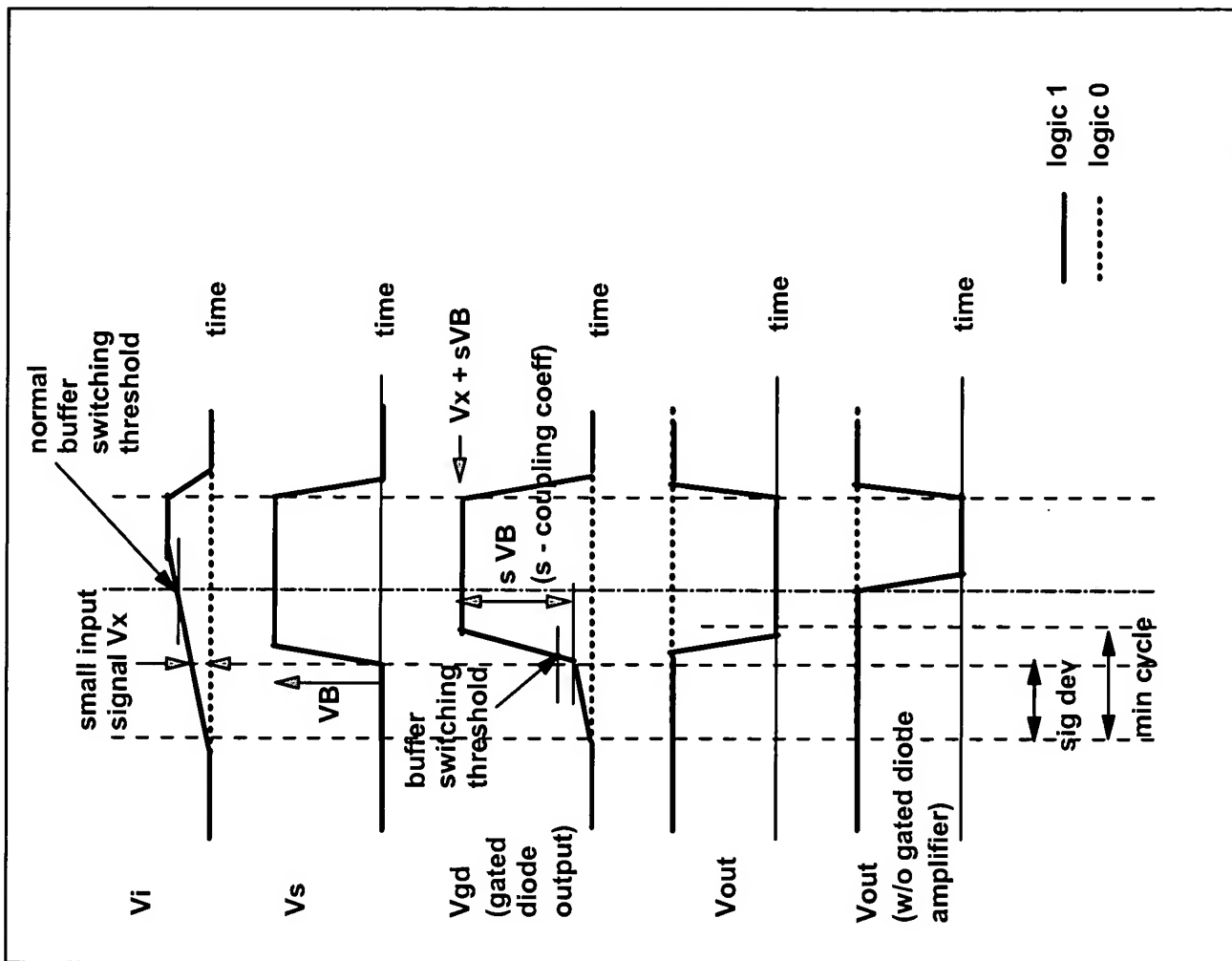


FIG. 14

FIG. 15

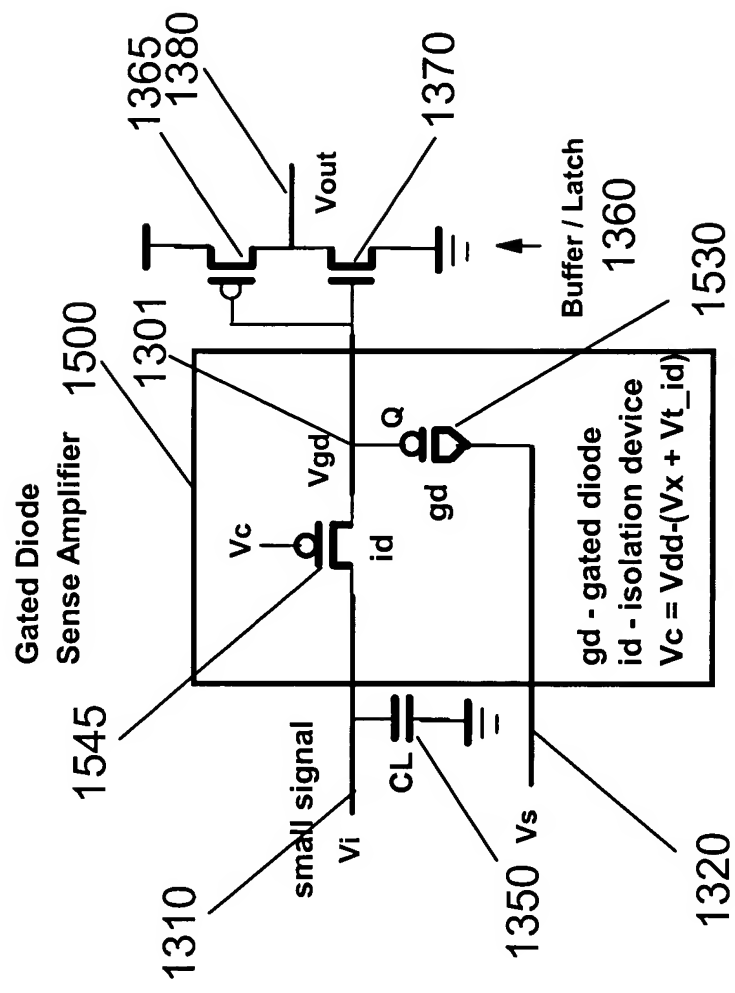


FIG. 16

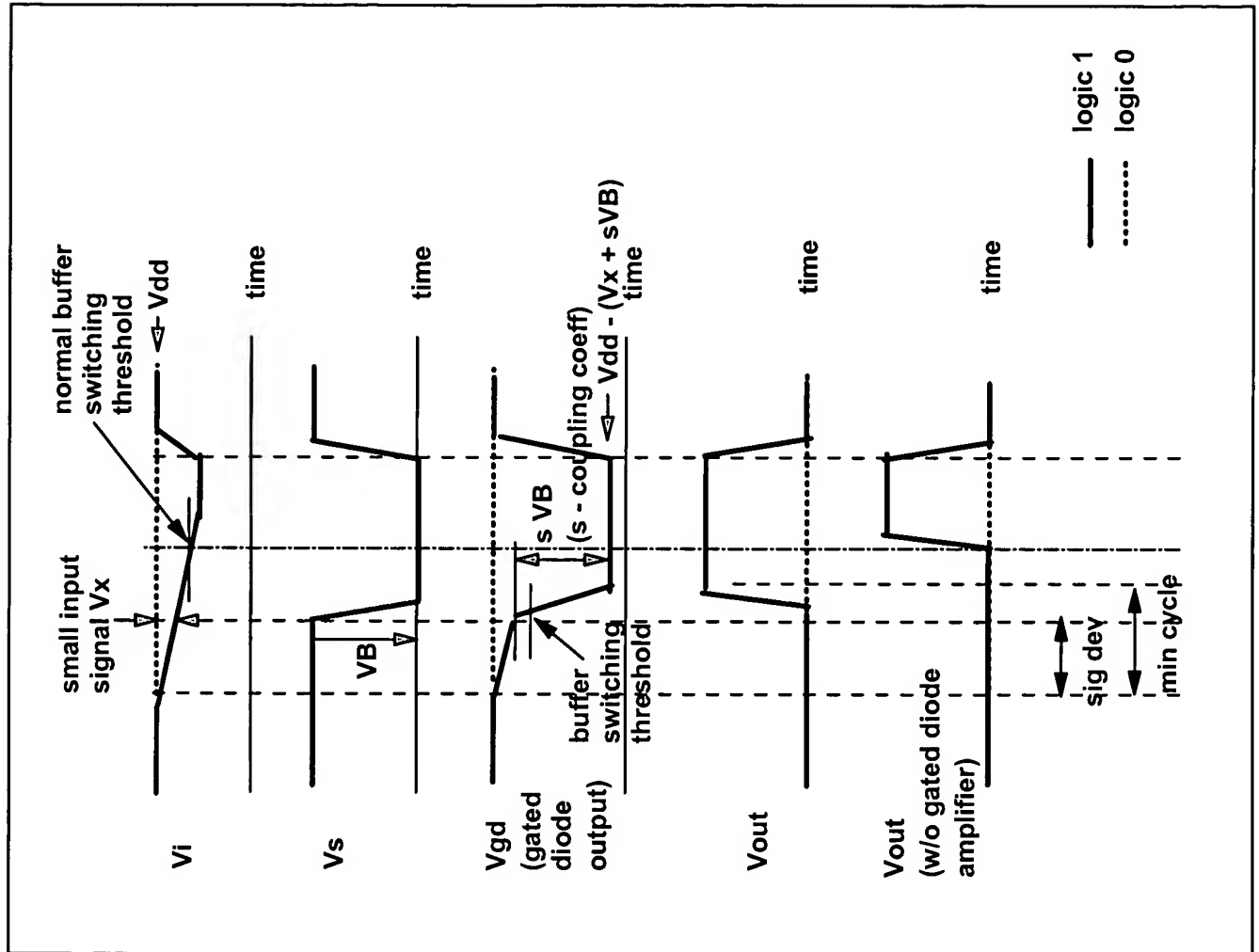
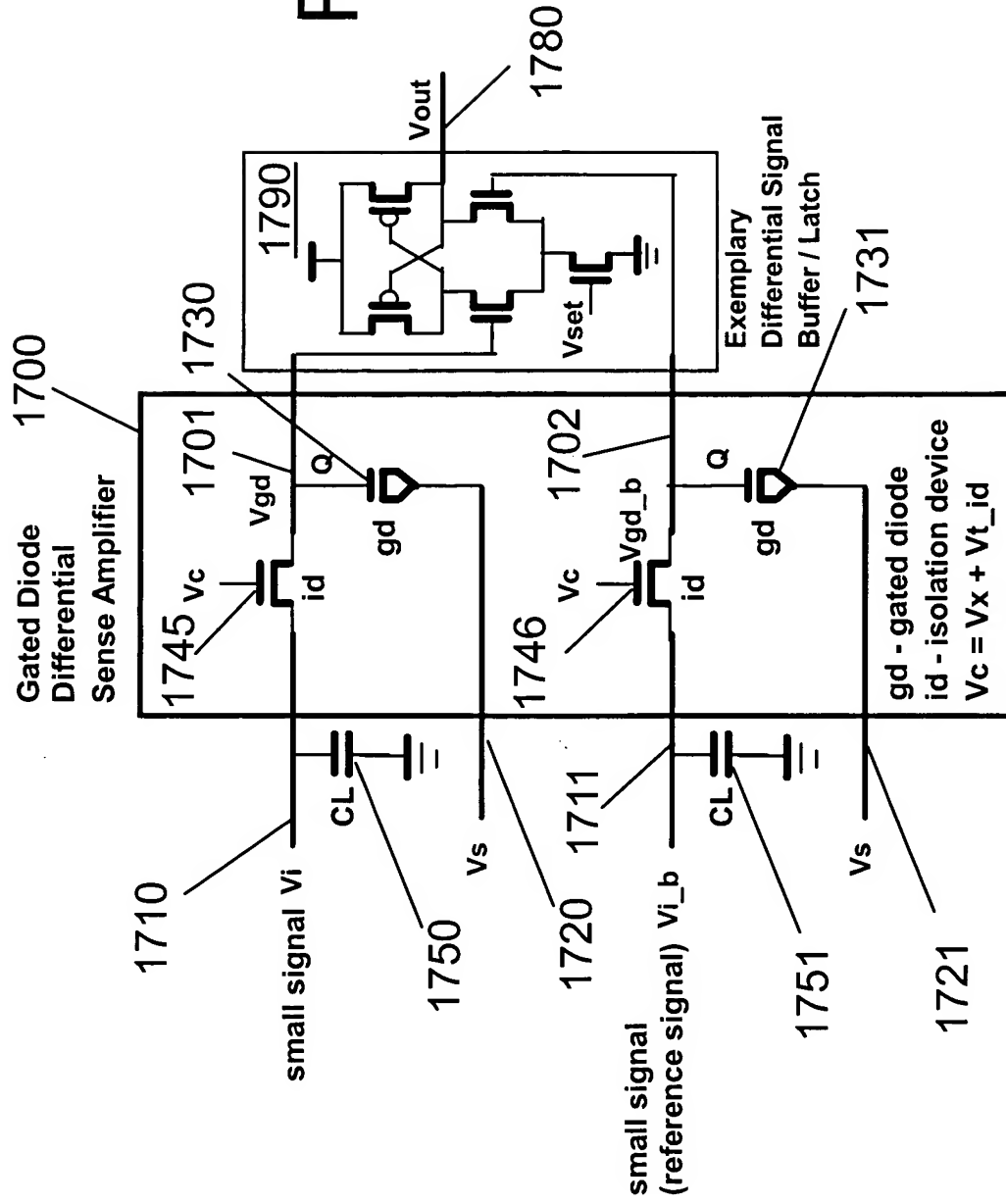




FIG. 17



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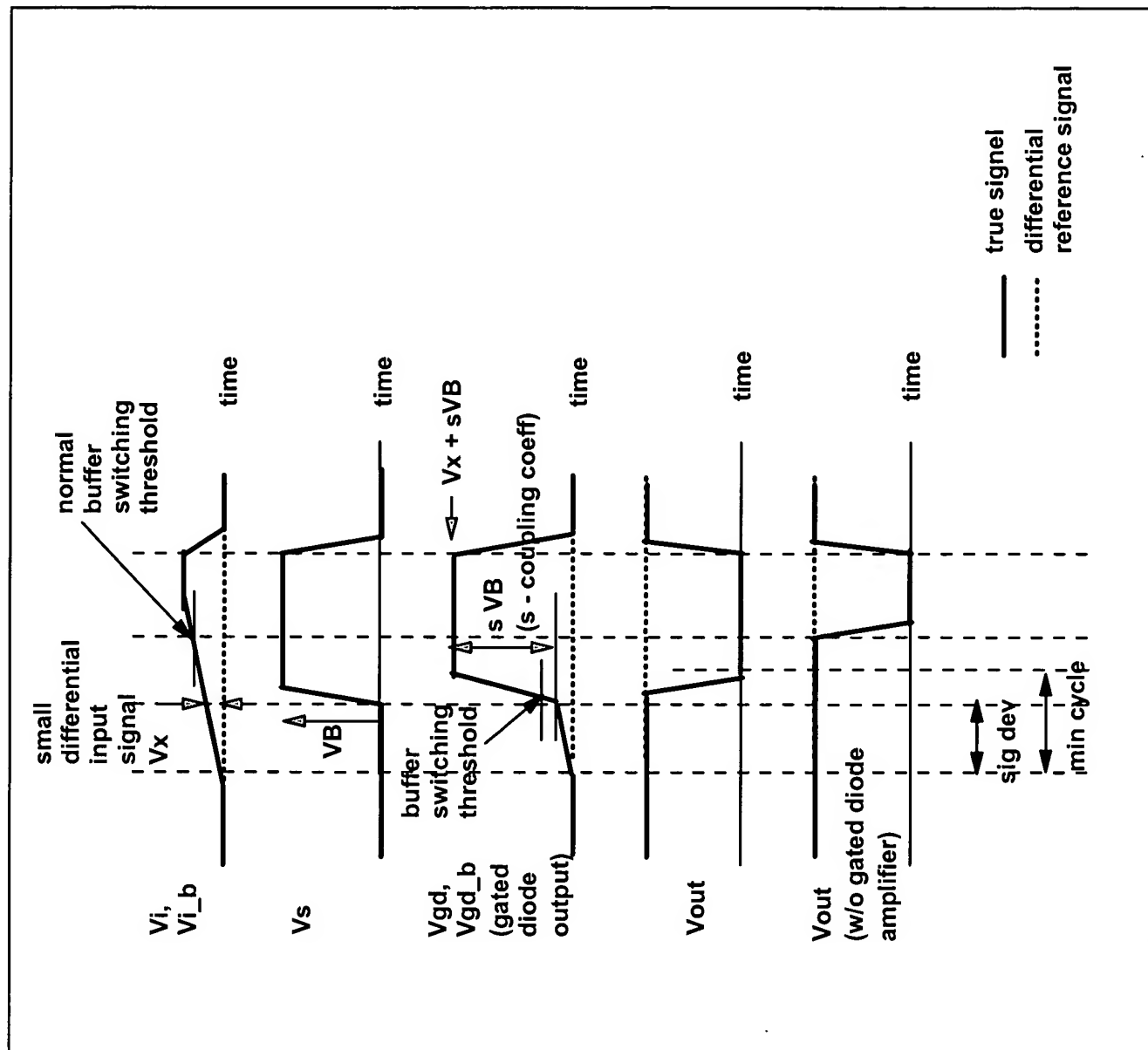
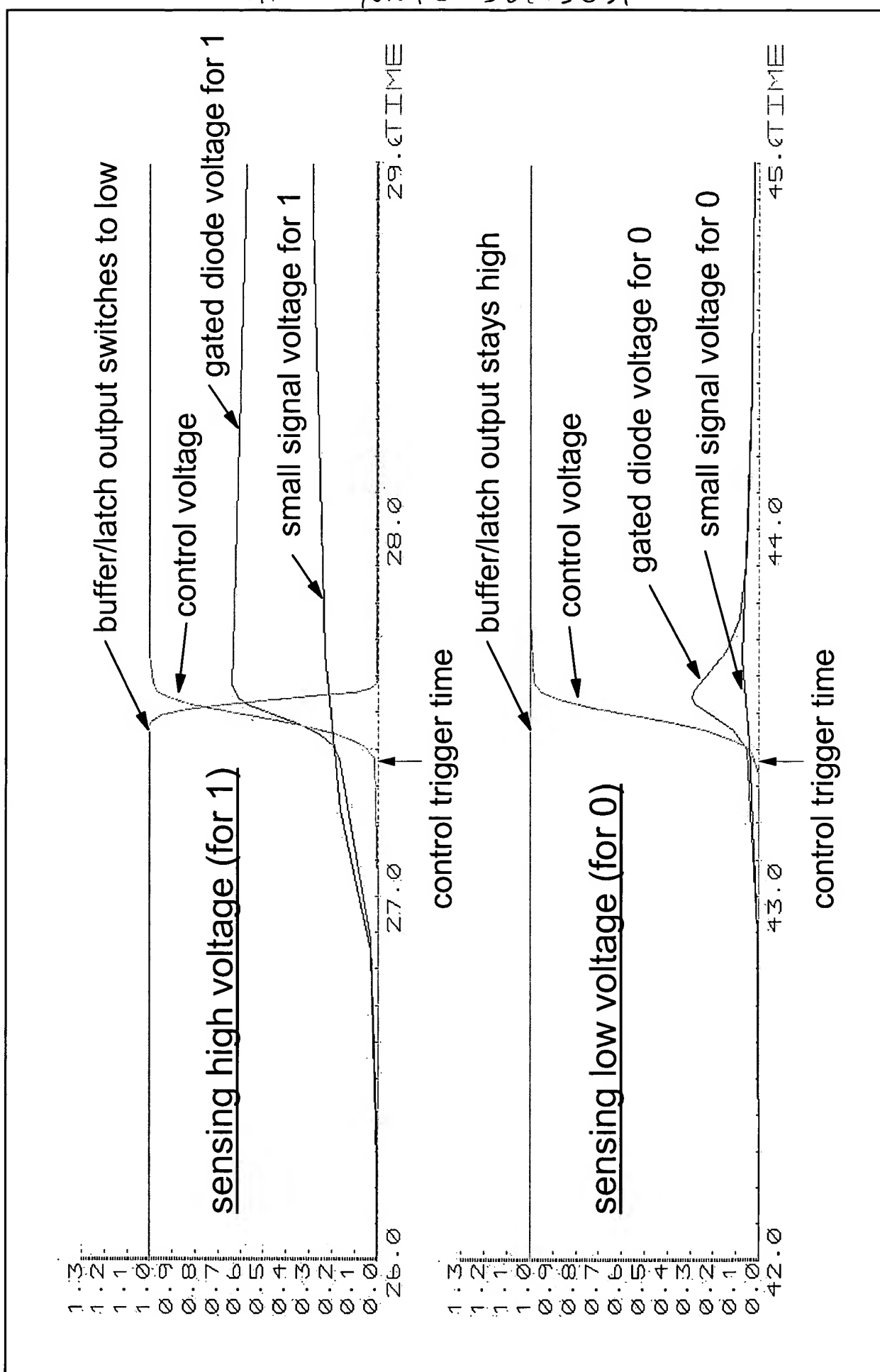


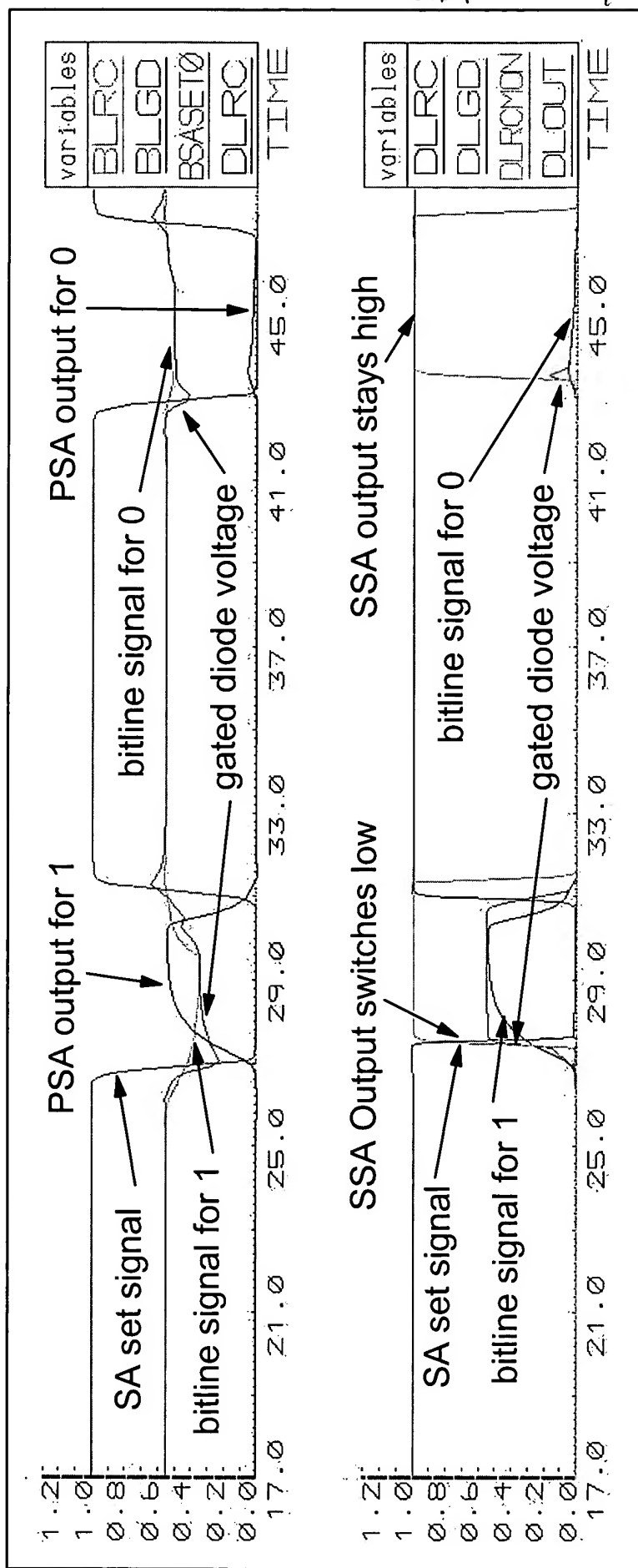
FIG. 18

# FIG. 19A



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# FIG. 19B



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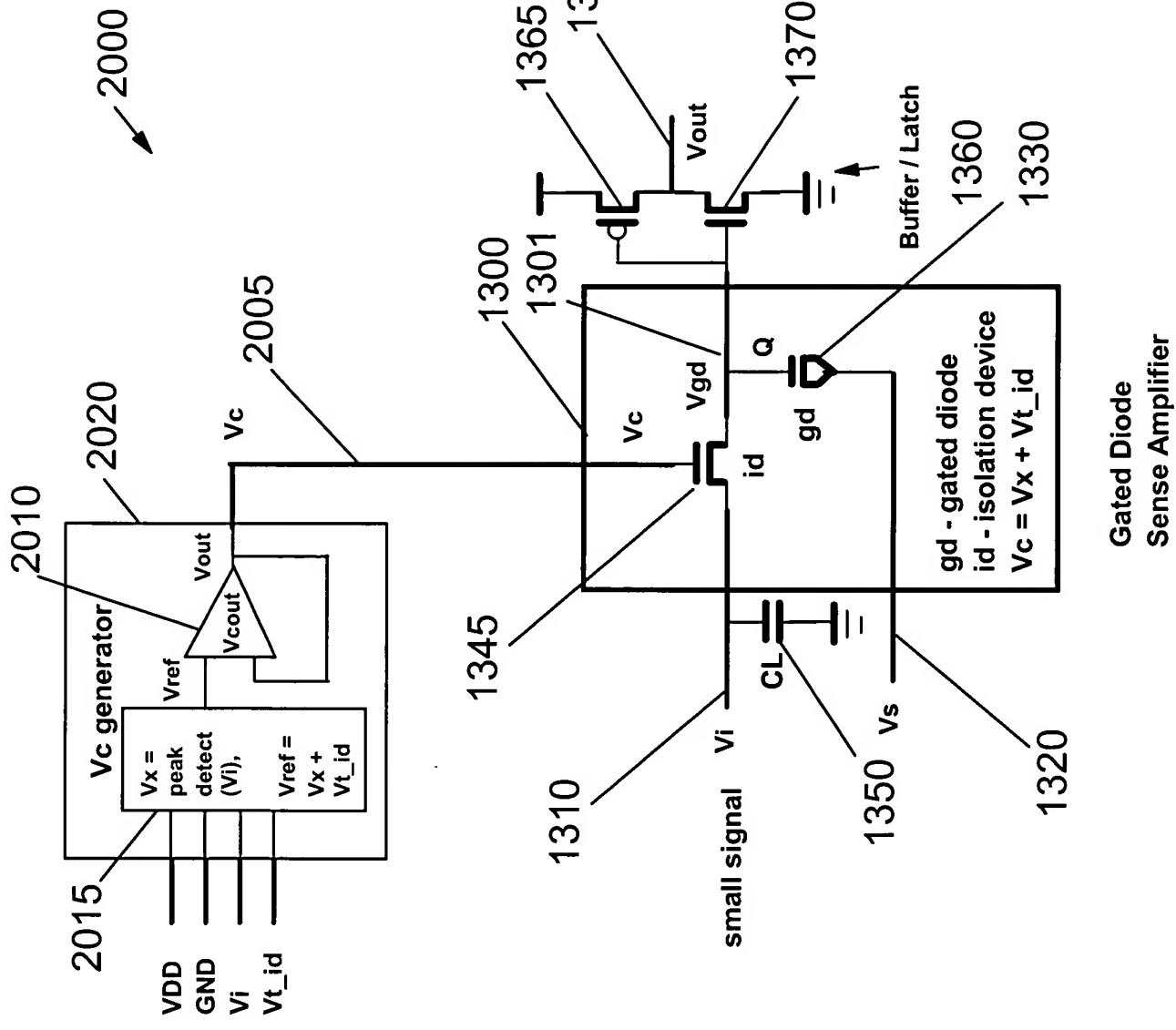


FIG. 20A

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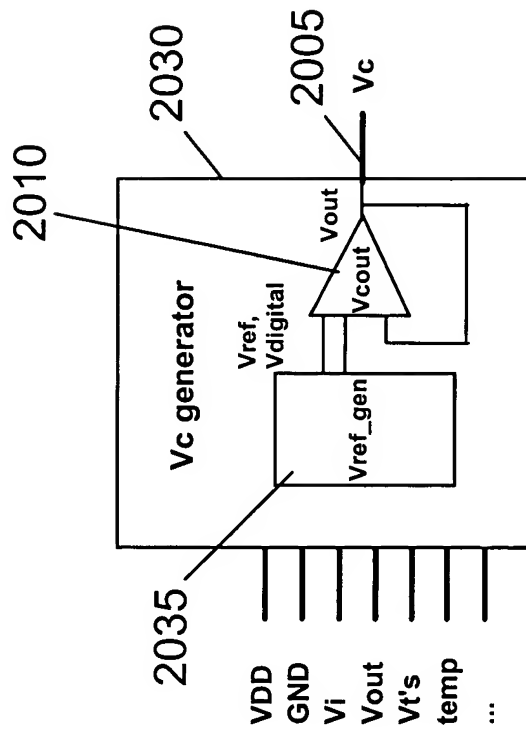


FIG. 20B